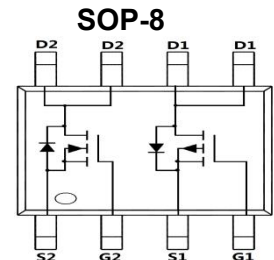
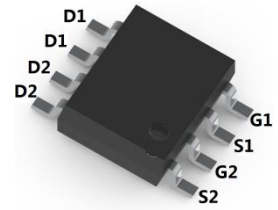


COMPLEMENTARY MOSFET
FEATURES

- $V_{DS}=20V, I_D=7.3A, R_{DS(ON)} \leq 23m\Omega @ V_{GS}=10V$
- $V_{DS}=-20V, I_D=-5A, R_{DS(ON)} \leq 53m\Omega @ V_{GS}=-4.5V$
- Low gate charge and Ultra low on-resistance
- For low Input Voltage inverter applications
- Surface Mount device

MECHANICAL DATA

- Case: SOP-8
- Case Material: Molded Plastic. UL flammability
- Classification Rating: 94V-0
- Weight: 0.3 grams (approximate)


MAXIMUM RATINGS (T_A = 25°C unless otherwise noted)

Parameter	Symbol	Max N-channel	Max P-channel	Unit
Drain-source voltage	V_{DS}	20	-30	V
Gate-source voltage	V_{GS}	±16	±12	V
Continuous drain current	I_D	$T_A = 25^\circ C$	-5	A
		$T_A = 70^\circ C$	-4.2	A
Pulsed drain current	I_{DM}	35	-25	A
Avalanche current	I_{AS}, I_{AR}	13	13	A
Avalanche energy L=0.1mH	E_{AS}, E_{AR}	25	25	mJ
Power dissipation	P_D	$T_A = 25^\circ C$	2	W
		$T_A = 70^\circ C$	1.44	W
Thermal resistance from Junction to ambient	$R_{\theta JA}$	110		°C/W
Thermal resistance from Junction to Lead	$R_{\theta JL}$	40		°C/W
Junction temperature	T_J	150		°C
Storage temperature	T_{STG}	-55 ~ +150		°C

N-CHANNEL ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Drain-Source breakdown voltage	$V_{(BR)DSS}^*$	20			V	$V_{GS}=0V, I_D=250\mu A$
Zero gate voltage drain current	I_{DSS}^*			1	μA	$V_{DS}=16V, V_{GS}=0V$
Gate-body leakage current	I_{GSS}^*			±100	nA	$V_{DS}=0V, V_{GS}=\pm 16V$
Gate-threshold voltage	$V_{GS(th)}^*$	0.6	1.25	2	V	$V_{DS}=V_{GS}, I_D=250\mu A$
On-State Drain Current	$I_{D(ON)}$	35			A	$V_{DS}=5V, V_{GS}=4.5V$
Drain-source on-resistance	$R_{DS(ON)}^*$		19	23	m Ω	$V_{GS}=10V, I_D=7.3A$
			28	33.6	m Ω	$V_{GS}=10V, I_D=7.3A, T_J = 125^\circ C$
			24	30	m Ω	$V_{GS}=4.5V, I_D=6.4A$
			67	84	m Ω	$V_{GS}=2.5V, I_D=2A$
Forward transconductance	g_{FS}		17		S	$V_{DS}=5V, I_D=7.3A$
Diode forward voltage	V_{SD}	0.7		1	V	$I_S=1A, V_{GS}=0V$
Diode forward current	I_S			3	A	
Input capacitance	C_{iss}		900	1100	pF	$V_{DS}=10V, V_{GS}=0V, f=1MHz$
Output capacitance	C_{oss}		162		pF	
Reverse transfer capacitance	C_{rss}		105		pF	
Gate resistance	R_g		0.9	1.35	Ω	$V_{DS}=0V, V_{GS}=0V, f=1MHz$
Total gate charge	Q_g		7.2	9	nC	$V_{GS}=4.5V, V_{DS}=10V, I_D=6.5A$
Total gate charge			15	18	nC	
Gate-source charge	Q_{gs}		1.8		nC	$V_{GS}=10V, V_{DS}=10V, I_D=6.5A$
Gate-drain charge	Q_{gd}		2.8		nC	
Turn-on delay time	$t_{d(on)}$		4.5		nS	$V_{GS}=10V, V_{DS}=10V, R_{GEN}=3\Omega, R_L=1.4\Omega$
Turn-on rise time	t_r		9.2		nS	
Turn-off delay time	$t_{d(off)}$		18.7		nS	
Turn-off fall time	t_f		3.3		nS	
Body Diode Reverse Recovery Time	t_{rr}		18		nS	$I_F=7.3A, di/dt=100A/\mu s$
Body Diode Reverse Recovery Charge	Q_{rr}		9.5		nC	$I_F=7.3A, di/dt=100A/\mu s$

*Pulse test ; Pulse width $\leq 300\mu s$, Duty cycle $\leq 0.5\%$.

COMPLEMENTARY MOSFET

N-CHANNEL TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

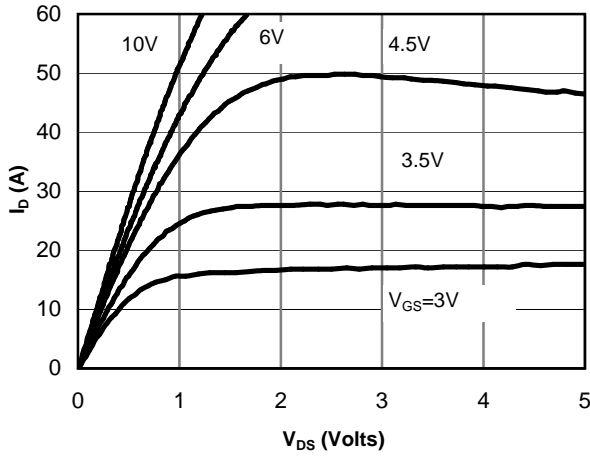


Figure 1: On-Region Characteristics

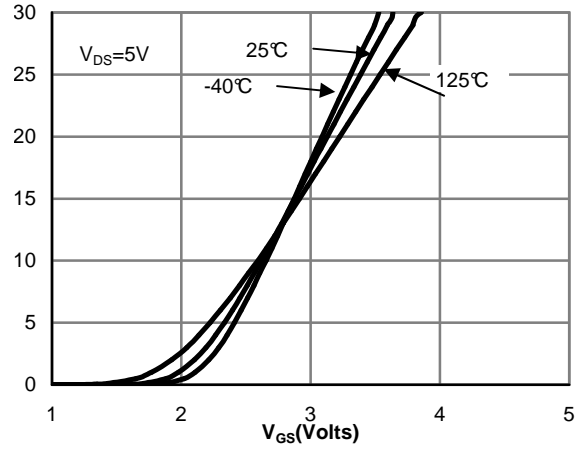


Figure 2: Transfer Characteristics

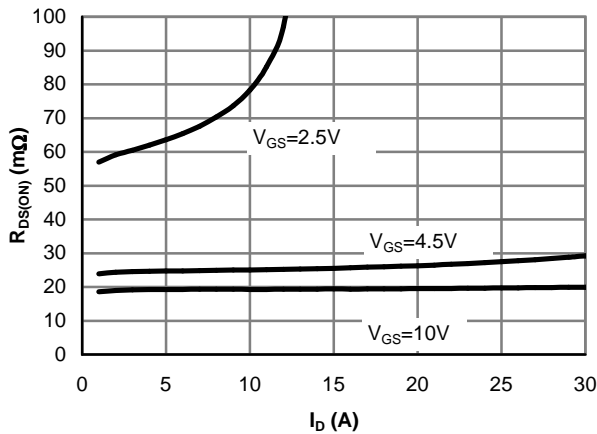


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

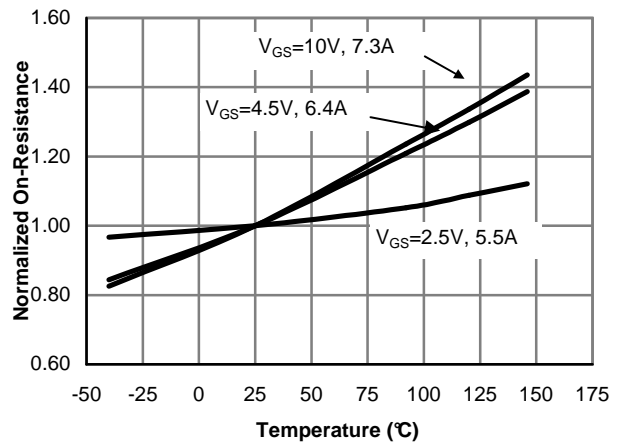


Figure 4: On-Resistance vs. Junction Temperature

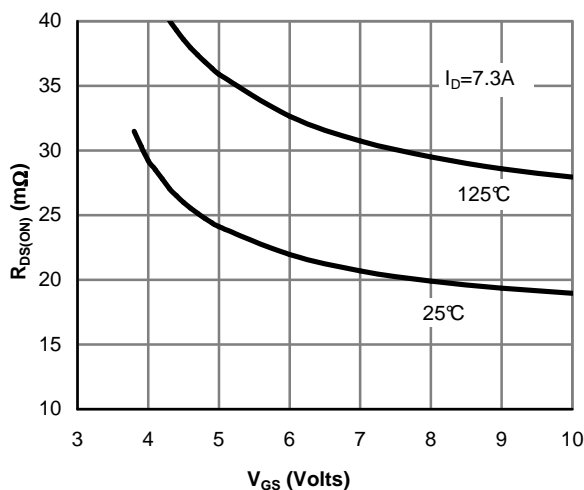


Figure 5: On-Resistance vs. Gate-Source Voltage

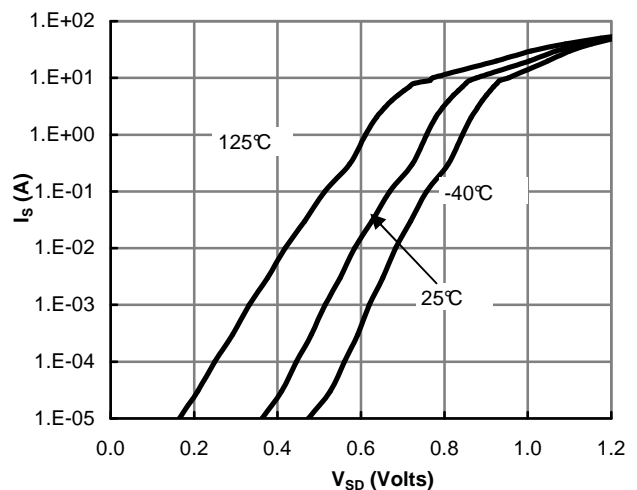


Figure 6: Body-Diode Characteristics

COMPLEMENTARY MOSFET

N-Channel TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

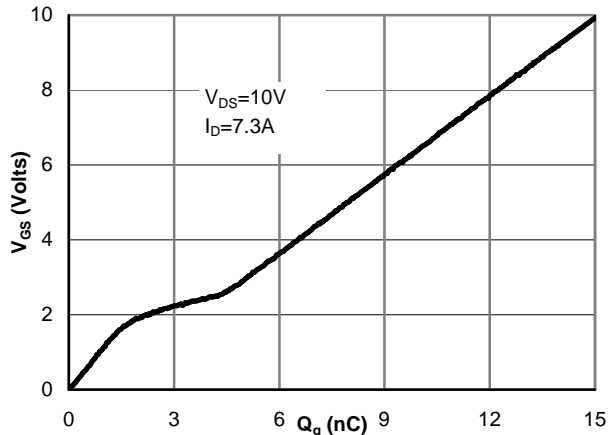


Figure 7: Gate-Charge Characteristics

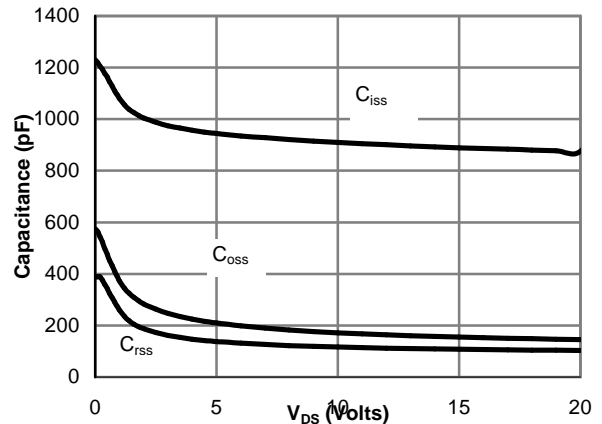


Figure 8: Capacitance Characteristics

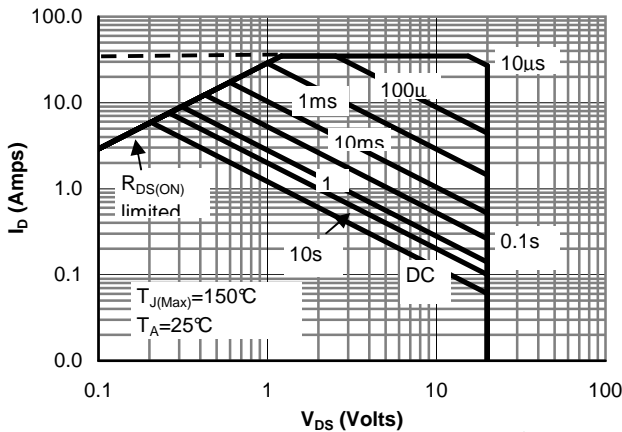


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

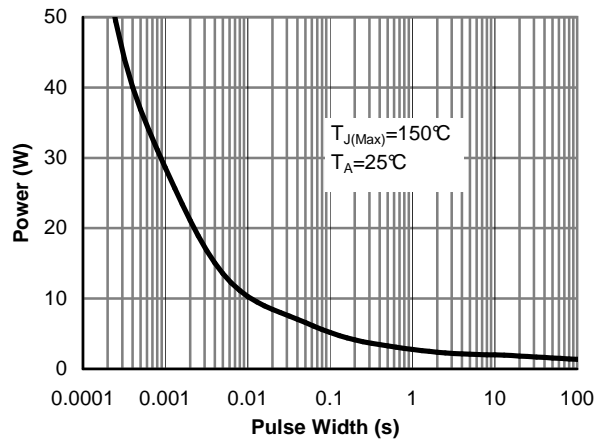


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

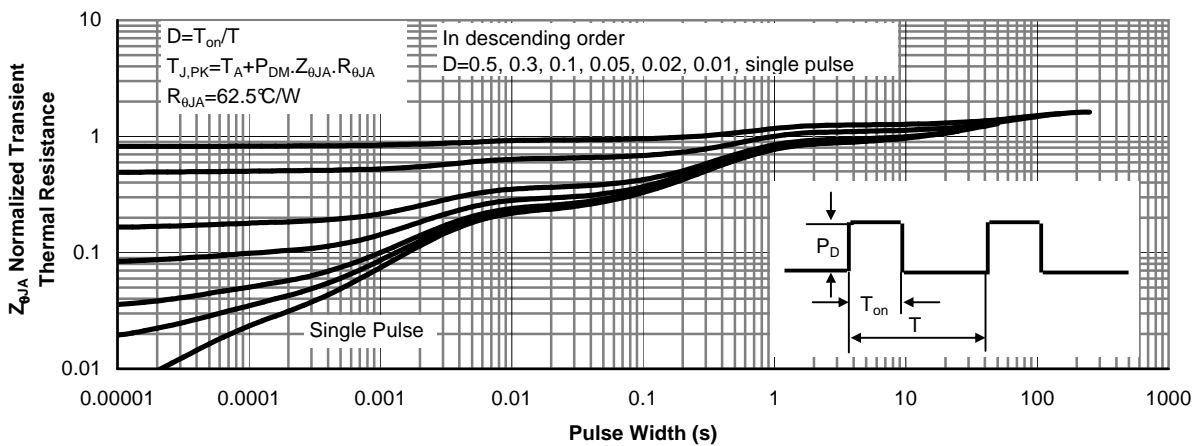
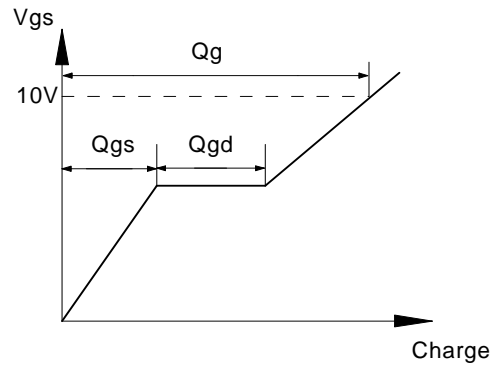
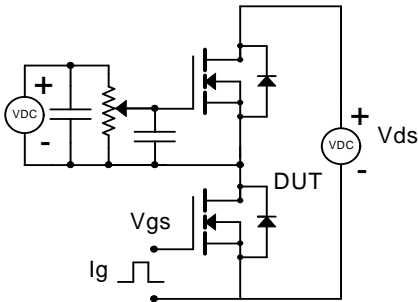


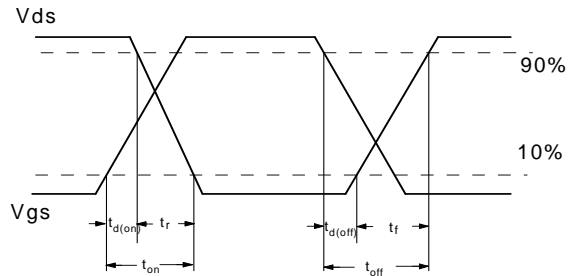
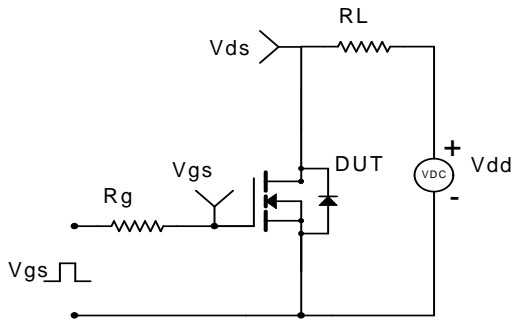
Figure 11: Normalized Maximum Transient Thermal Impedance

COMPLEMENTARY MOSFET

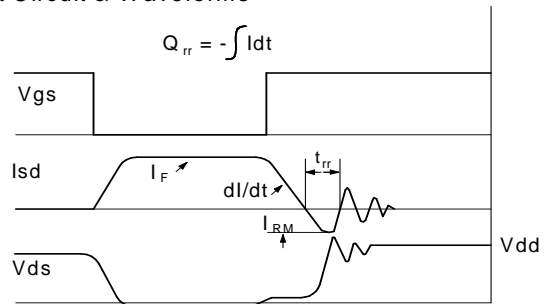
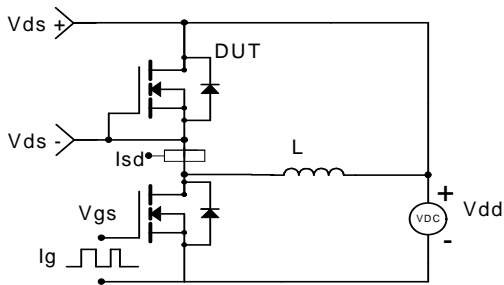
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



COMPLEMENTARY MOSFET
P-CHANNEL ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Drain-Source breakdown voltage	V _{(BR)DSS} *	-20			V	V _{GS} =0V, I _D =-250μA
Zero gate voltage drain current	I _{DSS} *			-1	μA	V _{DS} =-16V, V _{GS} =0V
Gate-body leakage current	I _{GSS} *			±100	nA	V _{DS} =0V, V _{GS} =±12V
Gate-threshold voltage	V _{GS(th)} *	-1.3	-0.9	-0.5	V	V _{DS} =V _{GS} , I _D =-250μA
On-State Drain Current	I _{D(ON)} *	-25			A	V _{DS} =-5V, V _{GS} =-4.5V
Drain-source on-resistance	R _{DS(ON)} *		44	53	mΩ	V _{GS} =-4.5V, I _D =-5A
			59	71	mΩ	V _{GS} =-4.5V, I _D =-5A, T _J =125°C
			67	87	mΩ	V _{GS} =-2.5V, I _D =-4.2A
Forward transconductance	g _{FS}		13		S	V _{DS} =-5V, I _D =-5A
Diode forward voltage	V _{SD}		-0.76	-1	V	I _S =-1A, V _{GS} =0V
Diode forward current	I _S			-2.5	A	
Input capacitance	C _{iss}		800	960	pF	V _{DS} =-10V, V _{GS} =0V, f=1MHz
Output capacitance	C _{oss}		131		pF	
Reverse transfer capacitance	C _{rss}		103		pF	
Gate resistance	R _g		6.7	10	Ω	V _{DS} =0V, V _{GS} =0V, f=1MHz
Total gate charge	Q _g		7.4		nC	V _{GS} =-4.5V, V _{DS} =-4.5V, I _D =-4.5A
Total gate charge			15.5		nC	V _{GS} =-4.5V, V _{DS} =-10V, I _D =-4.5A
Gate-source charge	Q _{gs}		1.3		nC	
Gate-drain charge	Q _{gd}		2.9		nC	
Turn-on delay time	t _{d(on)}		4.4		nS	V _{GS} =-4.5V, V _{DS} =-10V, R _{GEN} =3Ω, R _L =2Ω
Turn-on rise time	t _r		7.6		nS	
Turn-off delay time	t _{d(off)}		44		nS	
Turn-off fall time	t _f		13.5		nS	
Body Diode Reverse Recovery Time	t _{rr}		20		nS	I _F =-5A, dI/dt=100A/μs
Body Diode Reverse Recovery Charge	Q _{rr}		9		nC	I _F =-5A, dI/dt=100A/μs

*Pulse test ; Pulse width ≤300μs, Duty cycle ≤ 0.5% .

COMPLEMENTARY MOSFET

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: P-CHANNEL

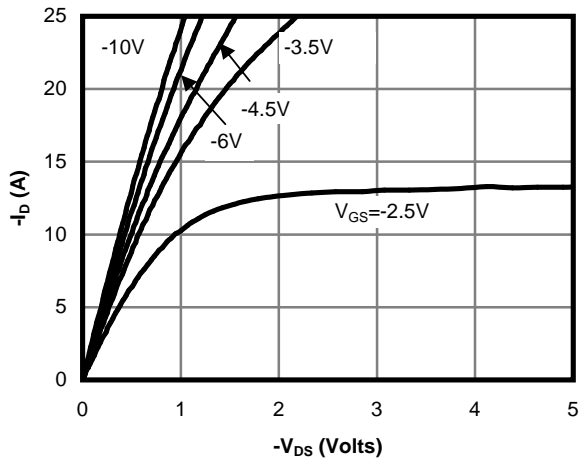


Fig 1: On-Region Characteristics

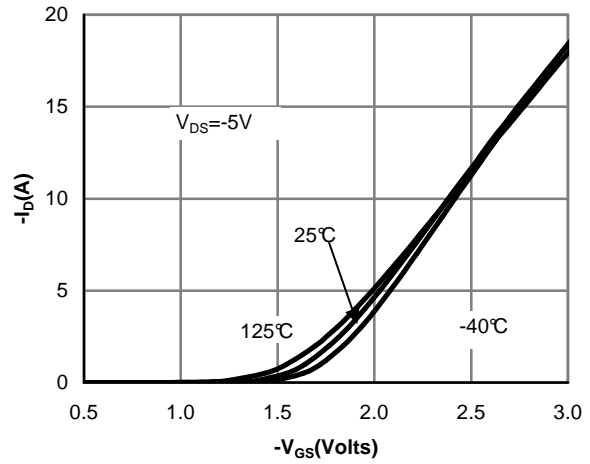


Figure 2: Transfer Characteristics

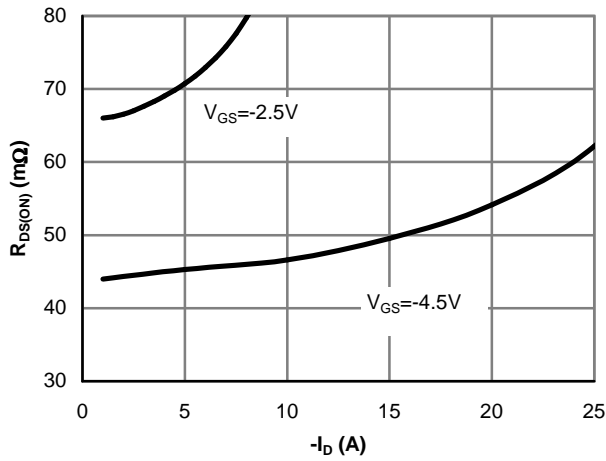


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

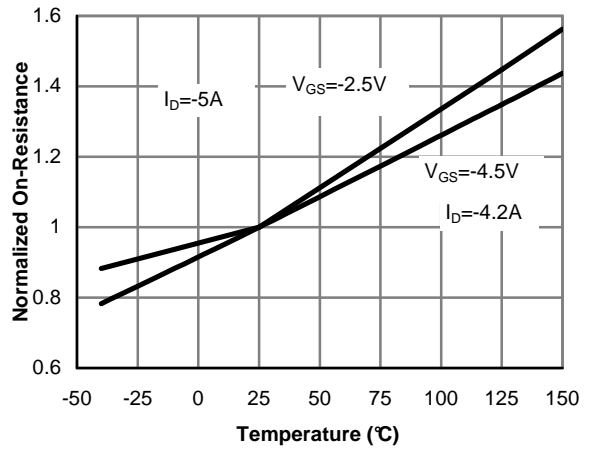


Figure 4: On-Resistance vs. Junction Temperature

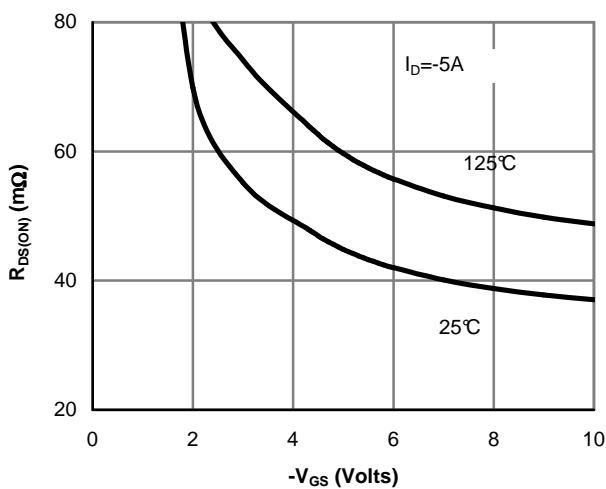


Figure 5: On-Resistance vs. Gate-Source Voltage

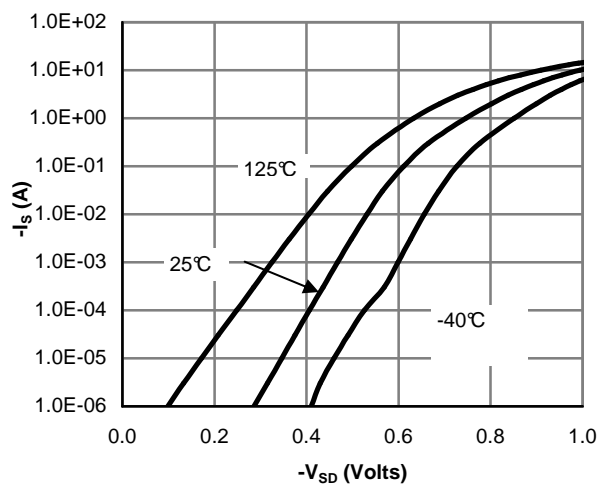


Figure 6: Body-Diode Characteristics

COMPLEMENTARY MOSFET

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS: P-CHANNEL

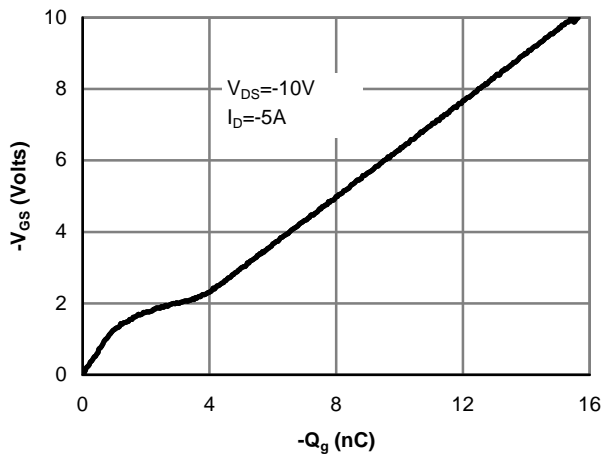


Figure 7: Gate-Charge Characteristics

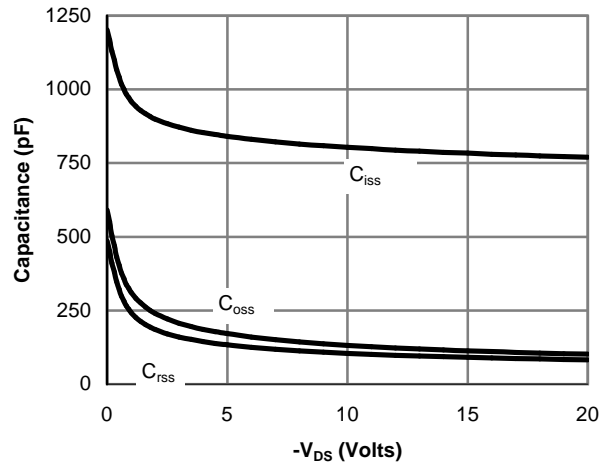


Figure 8: Capacitance Characteristics

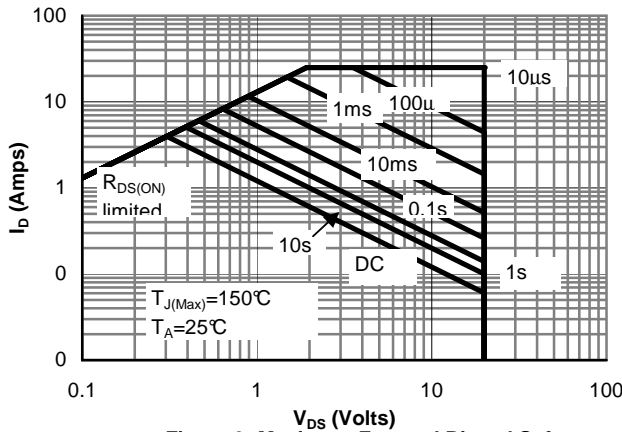


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

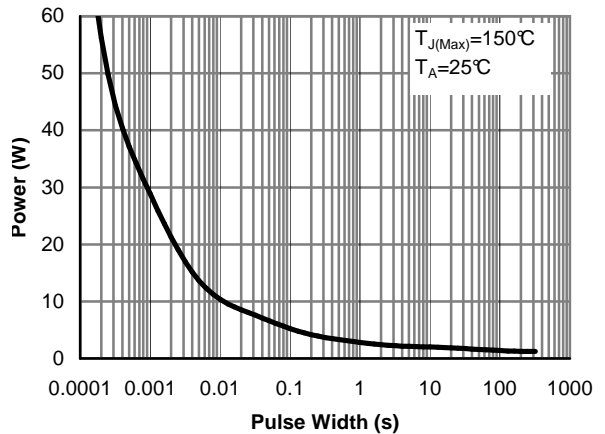


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

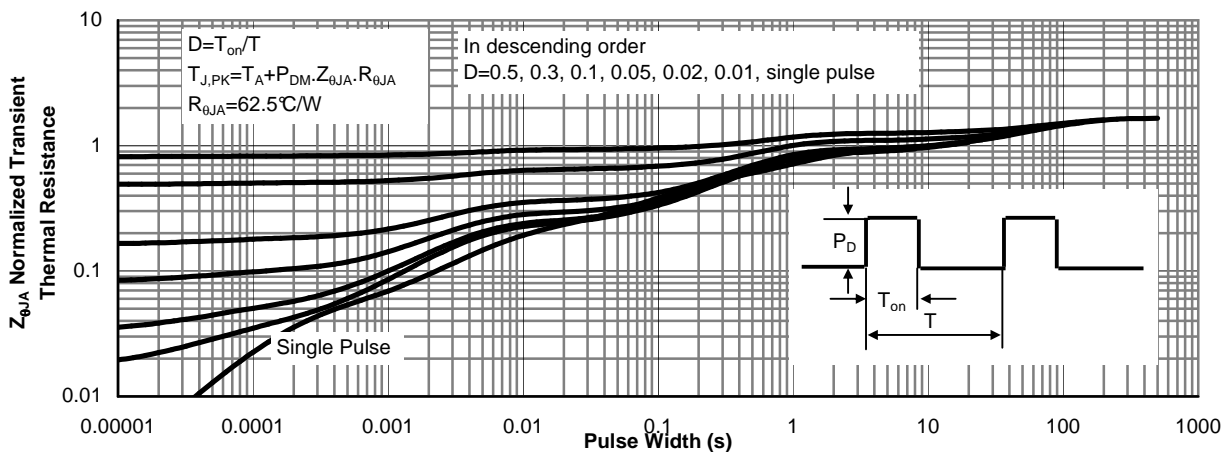
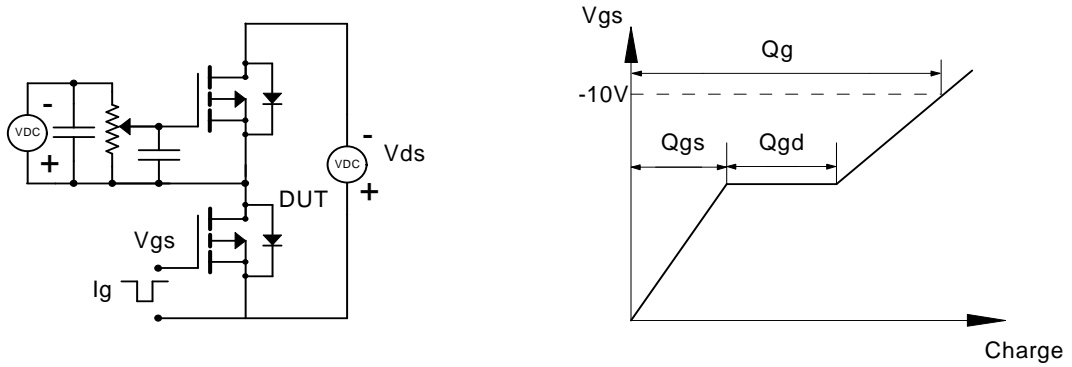


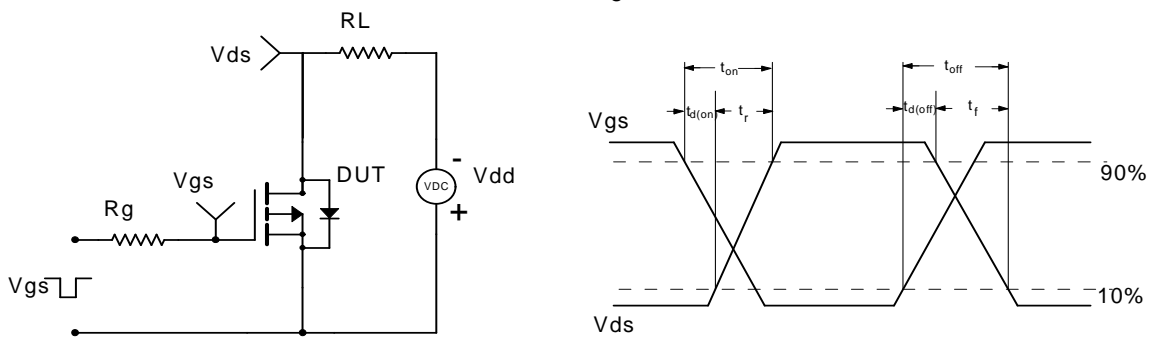
Figure 11: Normalized Maximum Transient Thermal Impedance

COMPLEMENTARY MOSFET

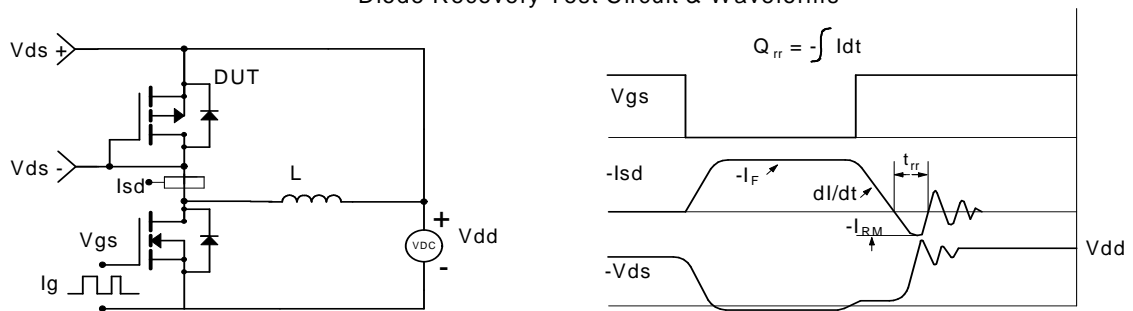
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

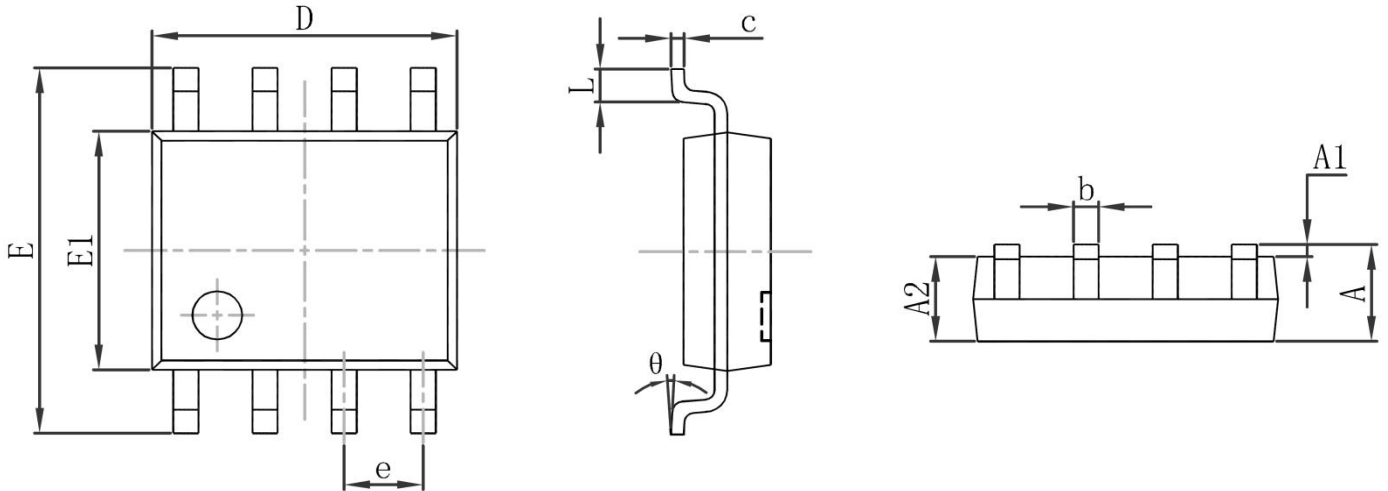


Diode Recovery Test Circuit & Waveforms



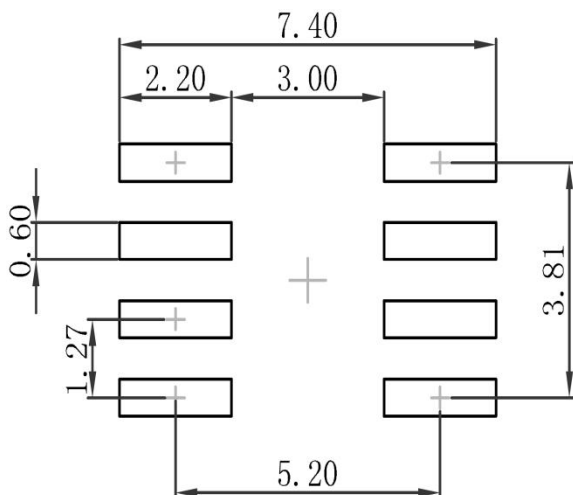
COMPLEMENTARY MOSFET

SOP-8 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.800	5.000	0.189	0.197
e	1.270(BSC)		0.050 (BSC)	
E	5.800	6.200	0.228	0.244
E1	3.800	4.000	0.150	0.157
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

SOP-8 Suggested Pad Layout



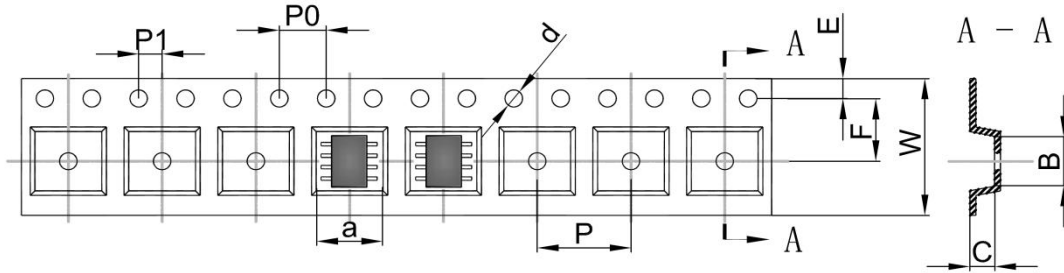
Note:

1. Controlling dimension: in millimeters
2. General tolerance: ±0.05mm
3. The pad layout is for reference purposes only

COMPLEMENTARY MOSFET

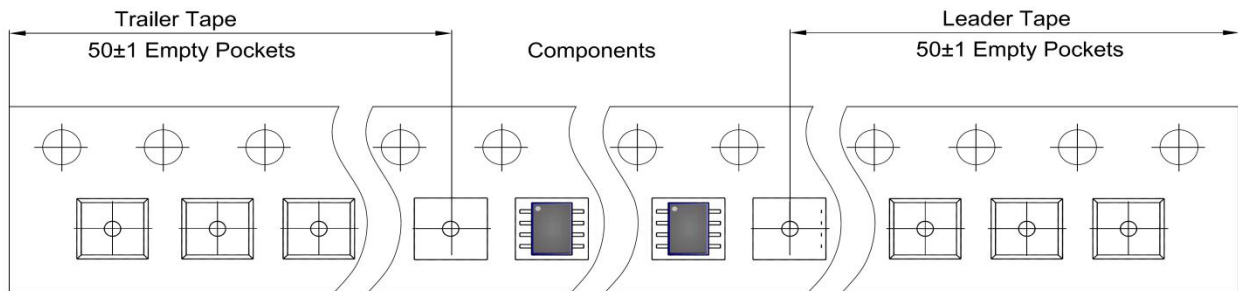
SOP-8 Tape and Reel

SOP-8 Embossed Carrier Tape

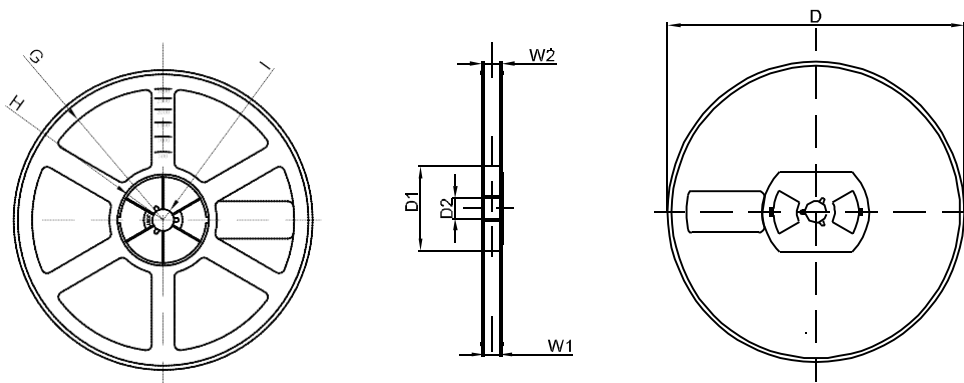


DIMENSIONS ARE IN MILLIMETER										
TYPE	A	B	C	d	E	F	P0	P	P1	W
SOP-8	6.40	5.40	2.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00
TOLERANCE	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1

SOP-8 Tape Leader and Trailer



SOP-8 Reel



DIMENSIONS ARE IN MILLIMETER								
REEL OPTION	D	D1	D2	G	H	I	W1	W2
13" DIA	Ø330.00	100.00	13.00	R151.00	R56.00	R6.50	12.40	17.60
TOLERANCE	±2	±1	±1	±1	±1	±1	±1	±1