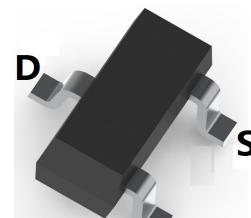


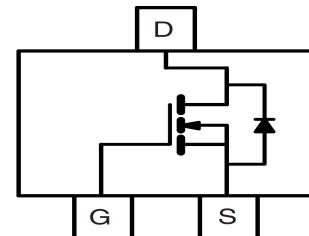
LOW VOLTAGE MOSFET (N-CHANNEL)

FEATURES

- Ultra low on-resistance: $V_{DS}=20V, R_{DS(ON)}=40m\Omega @ V_{GS}=4.5V, I_D=3A$
- For Low power DC to DC converter application
- For Load switch application
- Surface Mount device



SOT-23



MECHANICAL DATA

- Case: SOT-23
- Case Material: Molded Plastic. UL flammability
- Classification Rating: 94V-0
- Weight: 0.008 grams (approximate)

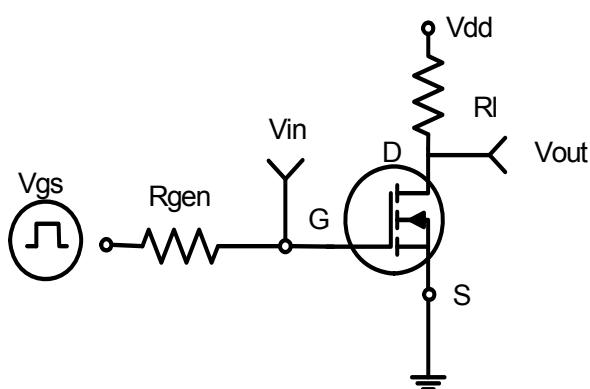
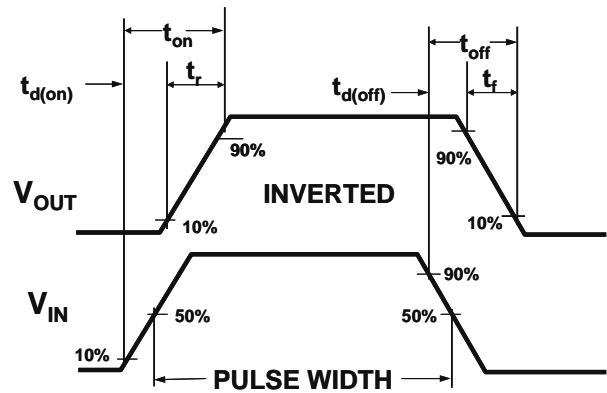
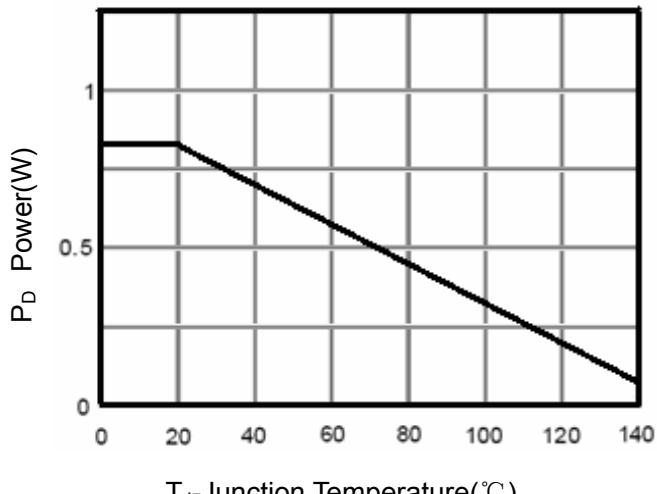
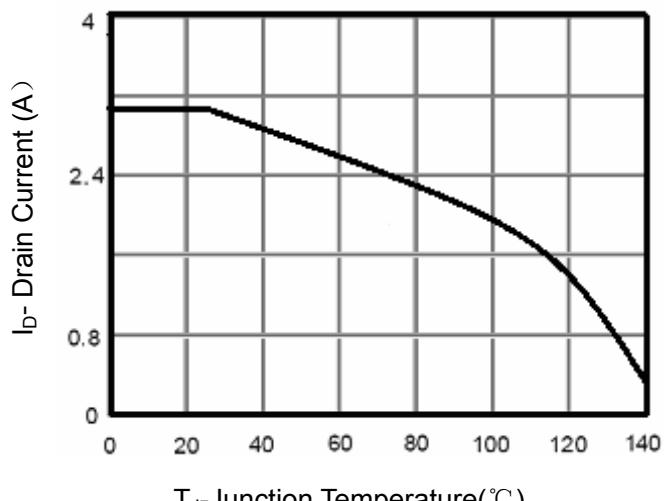
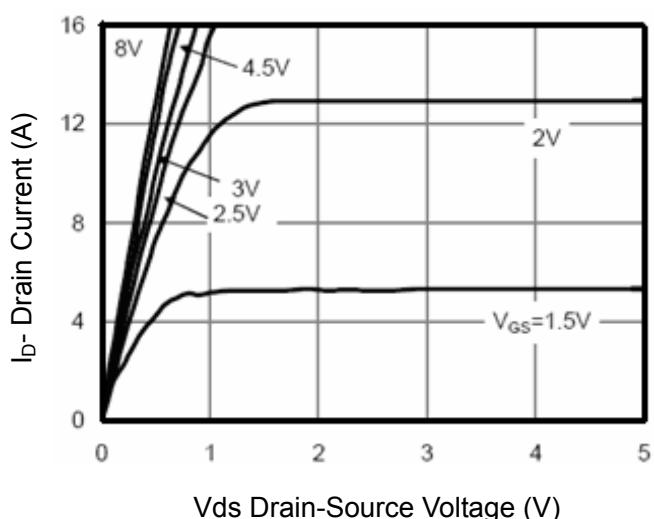
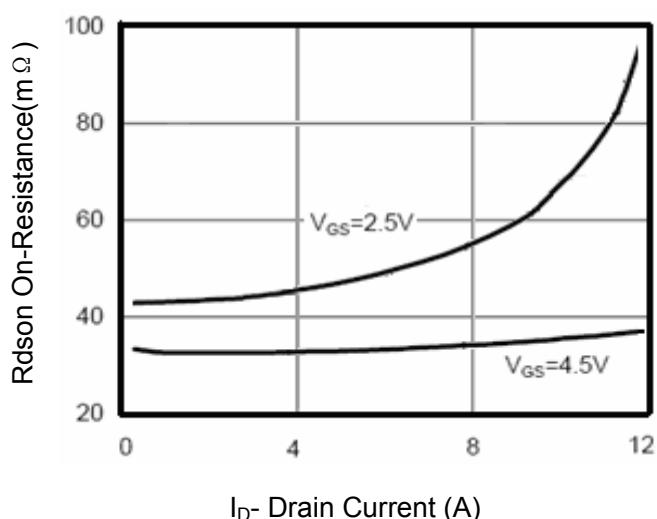
MAXIMUM RATINGS ($T_A = 25^\circ C$ unless otherwise noted)

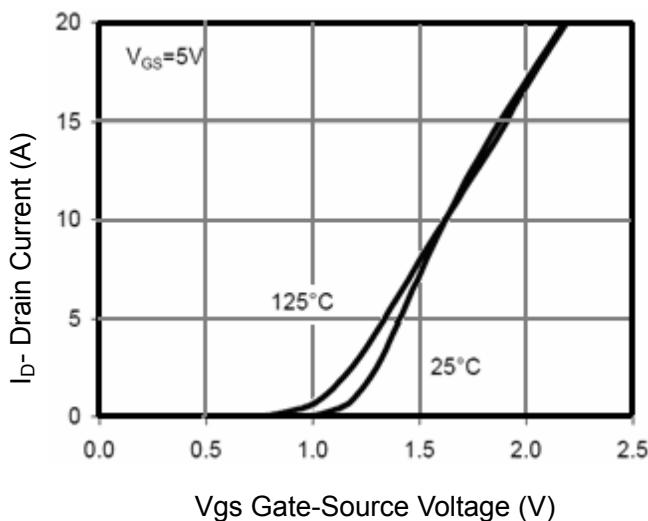
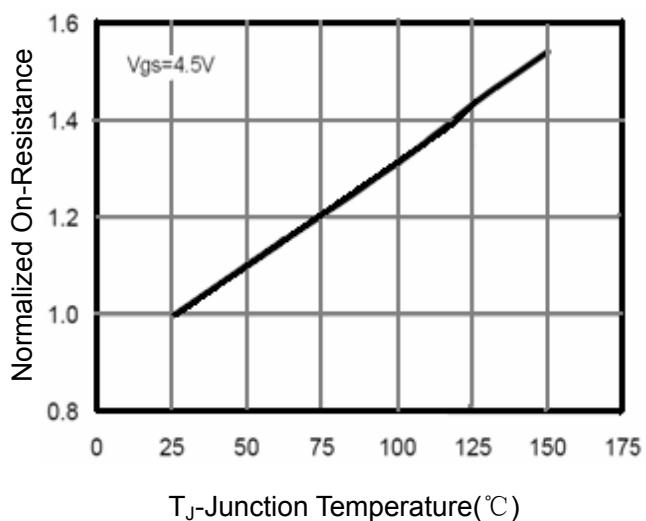
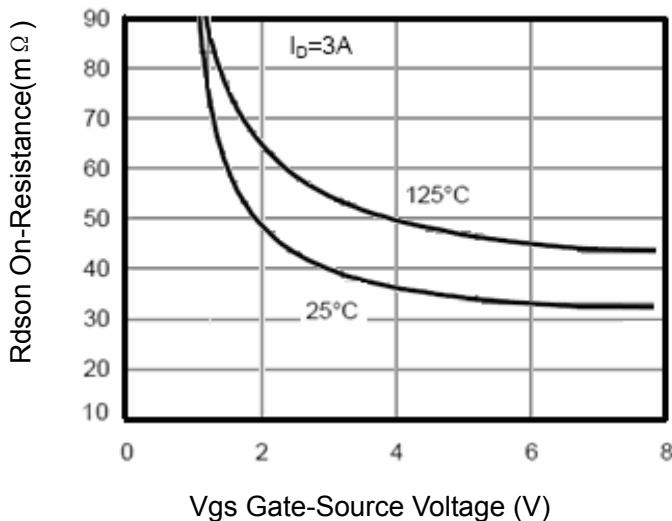
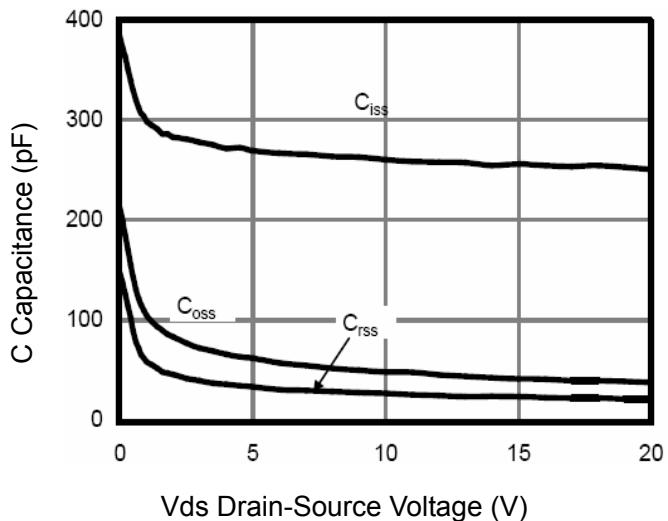
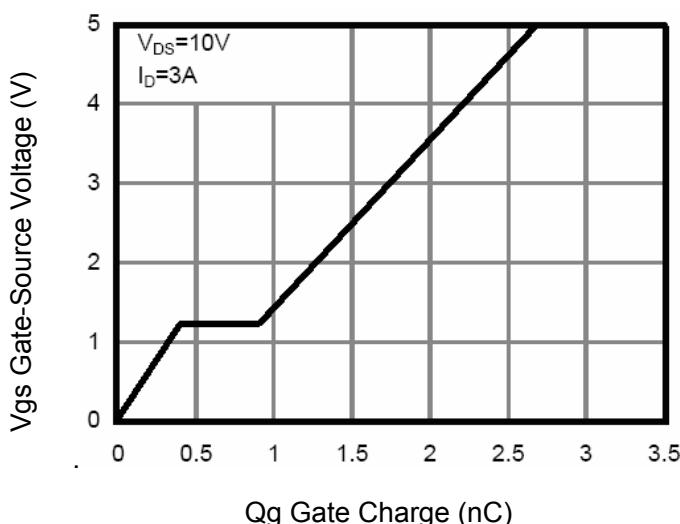
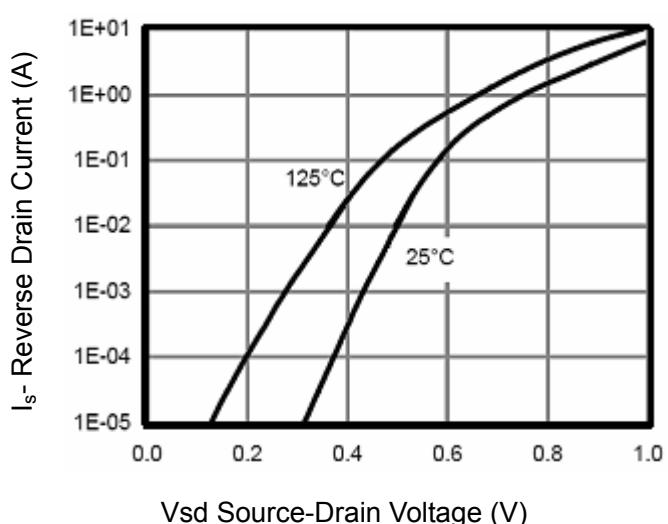
Parameter	Symbol	Value	Unit
Drain-source voltage	V_{DS}	20	V
Gate-source voltage	V_{GS}	± 10	V
Continuous drain current	I_D	3	A
Pulsed drain current (Note 1)	I_{DM}	12	A
Power dissipation	P_D	1.0	W
Thermal resistance from Junction to ambient	$R_{\theta JA}$	125	$^\circ C/W$
Junction temperature	T_J	150	$^\circ C$
Storage temperature	T_{STG}	-55 ~ +150	$^\circ C$

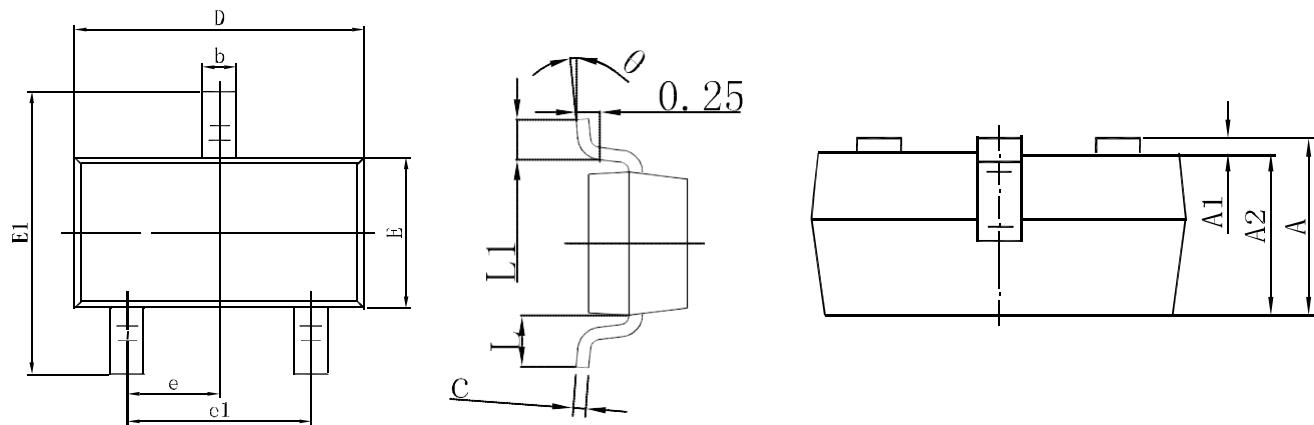
ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ C$ unless otherwise specified)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Drain-Source breakdown voltage	$V_{(BR)DSS}$	20			V	$V_{GS}=0V, I_D=10\mu A$
Zero gate voltage drain current	I_{DSS}			1	μA	$V_{DS}=20V, V_{GS}=0V$
Gate-body leakage current	I_{GSS}			± 100	nA	$V_{DS}=0V, V_{GS}=\pm 10V$
Gate-threshold voltage (note 1)	$V_{GS(th)}$	0.4		1.2	V	$V_{DS}=V_{GS}, I_D=250\mu A$
Drain-source on-resistance (note 1)	$R_{DS(ON)}$		40	55	$m\Omega$	$V_{GS}=4.5V, I_D=3.0A$
			55	80	$m\Omega$	$V_{GS}=2.5V, I_D=2.0A$
Forward transconductance (note 1)	g_{FS}		8.5		S	$V_{DS}=5V, I_D=3.0A$
Input capacitance	C_{iss}		237		pF	$V_{DS}=10V, V_{GS}=0V, f=1MHz$
Output capacitance	C_{oss}		80		pF	
Reverse transfer capacitance	C_{rss}		35		pF	
Turn-on delay time	$t_{d(on)}$		23		nS	
Turn-on rise time	t_r		11		nS	$V_{DD}=10V, V_{GS}=4.5V, R_{GEN}=6\Omega, I_D=3.0A$
Turn-off delay time	$t_{d(off)}$		34		nS	
Turn-off fall time	t_f		36		nS	
Total gate charge	Q_g		2.7		nC	
Gate-source charge	Q_{gs}		0.4		nC	$V_{DD}=10V, V_{GS}=4.5V, I_D=3.0A$
Gate-drain charge	Q_{gd}		0.8		nC	
Diode forward voltage (note 1)	V_{SD}			1.2	V	
Diode forward current	I_s			3.0	A	

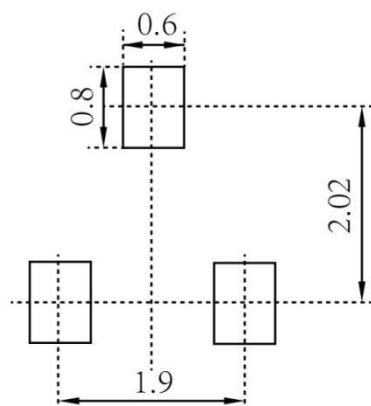
Note:1. Pulse test ; Pulse width $\leq 300\mu s$, Duty cycle $\leq 2\%$.

**LOW VOLTAGE MOSFET (N-CHANNEL)
Typical Electrical and Thermal Characteristics**

Figure 1:Switching Test Circuit

Figure 2:Switching Waveforms

Figure 3 Power Dissipation

Figure 4 Drain Current

Figure 5 Output Characteristics

Figure 6 Drain-Source On-Resistance

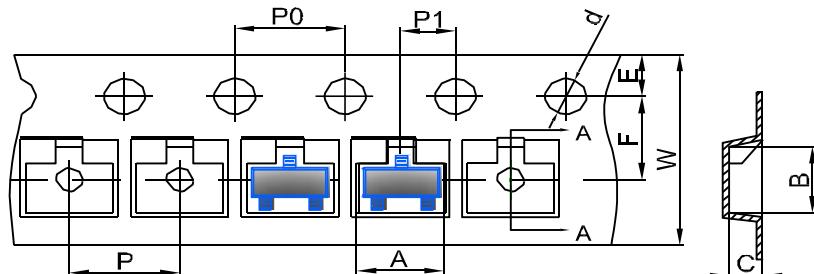
LOW VOLTAGE MOSFET (N-CHANNEL)

Figure 7 Transfer Characteristics

Figure 8 Drain-Source On-Resistance

Figure 9 R_{DSON} vs V_{GS}

Figure 10 Capacitance vs V_{DS}

Figure 11 Gate Charge

Figure 12 Source- Drain Diode Forward

LOW VOLTAGE MOSFET (N-CHANNEL)
SOT-23 Package Outline Dimensions


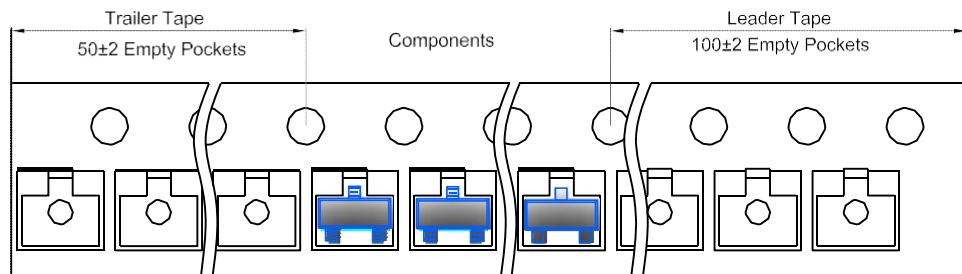
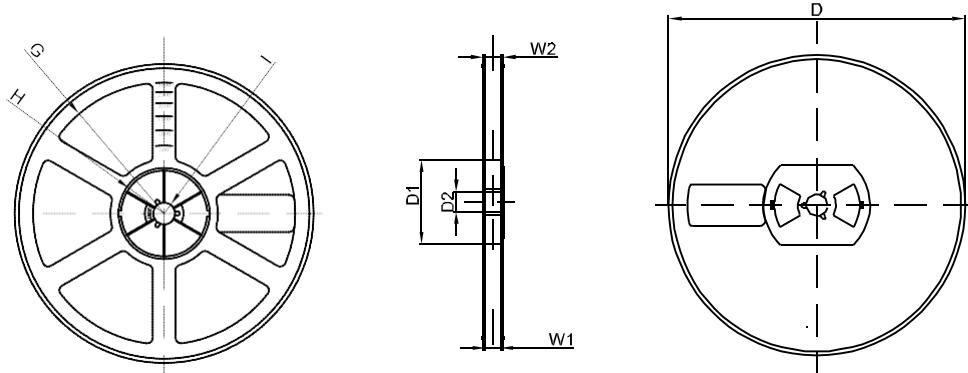
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

SOT-23 Suggested Pad Layout

Note:

1. Controlling dimension: in millimeters
2. General tolerance: $\pm 0.05\text{mm}$
3. The pad layout is for reference purposes only

LOW VOLTAGE MOSFET (N-CHANNEL)
SOT-23 Tape and Reel
SOT-23 Embossed Carrier Tape


TYPE	DIMENSIONS ARE IN MILLIMETER									
	A	B	C	d	E	F	P0	P	P1	W
SOT-23	3.15	2.77	1.22	Ø1.50	1.75	3.50	4.00	4.00	2.00	8.00
TOLERANCE	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1

SOT-23 Tape Leader and Trailer

SOT-23 Reel


REEL OPTION	DIMENSIONS ARE IN MILLIMETER							
	D	D1	D2	G	H	I	W1	W2
7" DIA	Ø178	54.40	13.00	R78	R25.60	R6.50	9.50	12.30
TOLERANCE	±2	±1	±1	±1	±1	±1	±1	±1