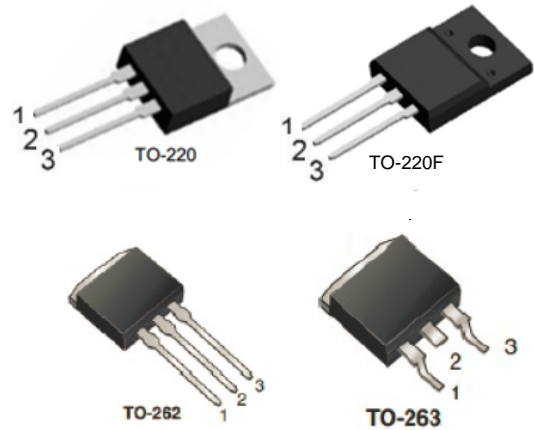
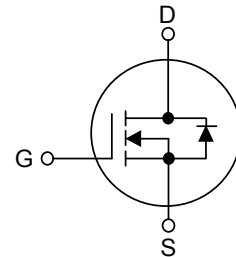


**650V N-Channel Power MOSFET**
**Features**

- $V_{DS}:650V, I_D:10A, R_{DS(ON)} < 1 \Omega @ V_{GS}=10V$
- Fast switching capability
- Low gate charge
- Lead free in compliance with EU RoHS directive.
- Green molding compound


**Ordering Information**

Part No.	Package	Packing
HKTF10N65-TU	TO-220	50pcs / Tube
HKTF10N65F-TU	TO-220F	50pcs / Tube
HKTY10N65-TU	TO-262	50pcs / Tube
HKTE10N65-TU	TO-263	50pcs / Tube
HKTE10N65-TR	TO-263	800pcs / 13" Reel

**Block Diagram**

**ABSOLUTE MAXIMUM RATINGS** ( $T_C=25^\circ C$ , unless otherwise specified)

PARAMETER	SYMBOL	RATINGS	UNIT	
Drain-Source Voltage	$V_{DSS}$	650	V	
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V	
Continuous Drain Current	$I_D$	10	A	
Pulsed Drain Current (Note 2)	$I_{DM}$	38	A	
Avalanche Energy	Single Pulsed (Note 3)	$E_{AS}$	608	mJ
Power Dissipation	TO-220/TO-262/TO-263	$P_D$	156	W
	TO-220F		50	W
Junction Temperature	$T_J$	+150	$^\circ C$	
Operating Temperature	$T_{OPR}$	-55 ~ +150	$^\circ C$	
Storage Temperature	$T_{STG}$	-55 ~ +150	$^\circ C$	

Notes: 1. Absolute maximum ratings are those values beyond which the device could be permanently damaged.

Absolute maximum ratings are stress ratings only and functional device operation is not implied.

2. Repetitive Rating : Pulse width limited by  $T_J$

3.  $L = 30mH, I_{AS} = 7.1A, V_{DD} = 50V, R_G = 25 \Omega$ , Starting  $T_J = 25^\circ C$

**650V N-Channel Power MOSFET**
**THERMAL DATA**

PARAMETER		SYMBOL	RATING	UNIT
Junction to Ambient	TO-220/TO-220F TO-262/TO-263	$\theta_{JA}$	62.5	°C/W
Junction to Case	TO-220	$\theta_{JC}$	0.85	°C/W
	TO-220F		2.6	

**ELECTRICAL CHARACTERISTICS** ( $T_C=25^\circ\text{C}$ , unless otherwise specified)

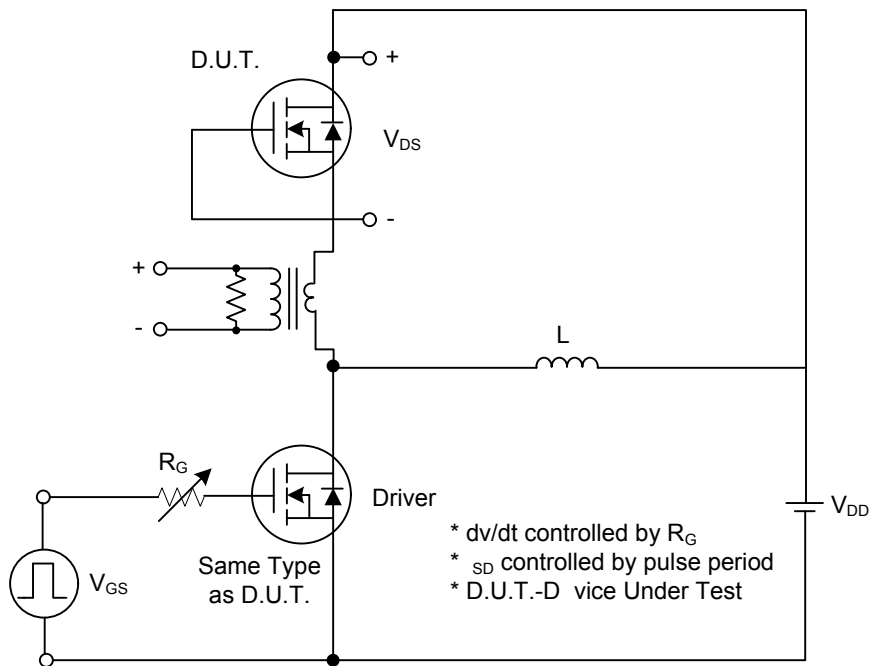
PARAMETER		SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT	
<b>OFF CHARACTERISTICS</b>								
Drain-Source Breakdown Voltage		$BV_{DSS}$	$V_{GS}=0V, I_D=250\mu A$	650			V	
Drain-Source Leakage Current		$I_{DSS}$	$V_{DS}=650V, V_{GS}=0V$			1	$\mu A$	
Gate- Source Leakage Current	Forward	$I_{GSS}$	$V_{GS}=30V, V_{DS}=0V$			100	nA	
	Reverse		$V_{GS}=-30V, V_{DS}=0V$			-100	nA	
<b>ON CHARACTERISTICS</b>								
Gate Threshold Voltage		$V_{GS(TH)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0		4.0	V	
Static Drain-Source On-State Resistance		$R_{DS(ON)}$	$X_{\theta S}=10V, I_D=5A$		0.88	1.0	$\Omega$	
<b>DYNAMIC CHARACTERISTICS</b>								
Input Capacitance		$C_{ISS}$	$V_{DS}=25V, V_{GS}=0V, f=1.0\text{ MHz}$		1200		pF	
Output Capacitance		$C_{OSS}$				166		pF
Reverse Transfer Capacitance		$C_{RSS}$				8		pF
<b>SWITCHING CHARACTERISTICS</b>								
Turn-On Delay Time		$t_{D(ON)}$	$V_{DD}=300V, I_D=10A, R_G=25\Omega$ (Note 1, 2)		40		ns	
Turn-On Rise Time		$t_R$				74		ns
Turn-Off Delay Time		$t_{D(OFF)}$				52		ns
Turn-Off Fall Time		$t_F$				35		ns
Total Gate Charge		$Q_G$	$V_{DS}=480V, I_D=10A, V_{GS}=10V$ (Note 1, 2)		24		nC	
Gate-Source Charge		$Q_{GS}$				8.0		nC
Gate-Drain Charge		$Q_{GD}$				7		nC
<b>DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS</b>								
Drain-Source Diode Forward Voltage		$V_{SD}$	$V_{GS}=0V, I_S=12A$			1.4	V	
Maximum Continuous Drain-Source Diode Forward Current		$I_S$				10	A	
Maximum Pulsed Drain-Source Diode Forward Current		$I_{SM}$				40	A	
Reverse Recovery Time		$t_{rr}$	$V_{GS}=0V, I_S=10A, dI_F/dt=100\text{ A}/\mu s$ (Note 1)		570		ns	
Reverse Recovery Charge		$Q_{RR}$			4.7		$\mu C$	

 Notes: 1. Pulse Test: Pulse width  $\leq 300\mu s$ , Duty cycle  $\leq 2\%$ .

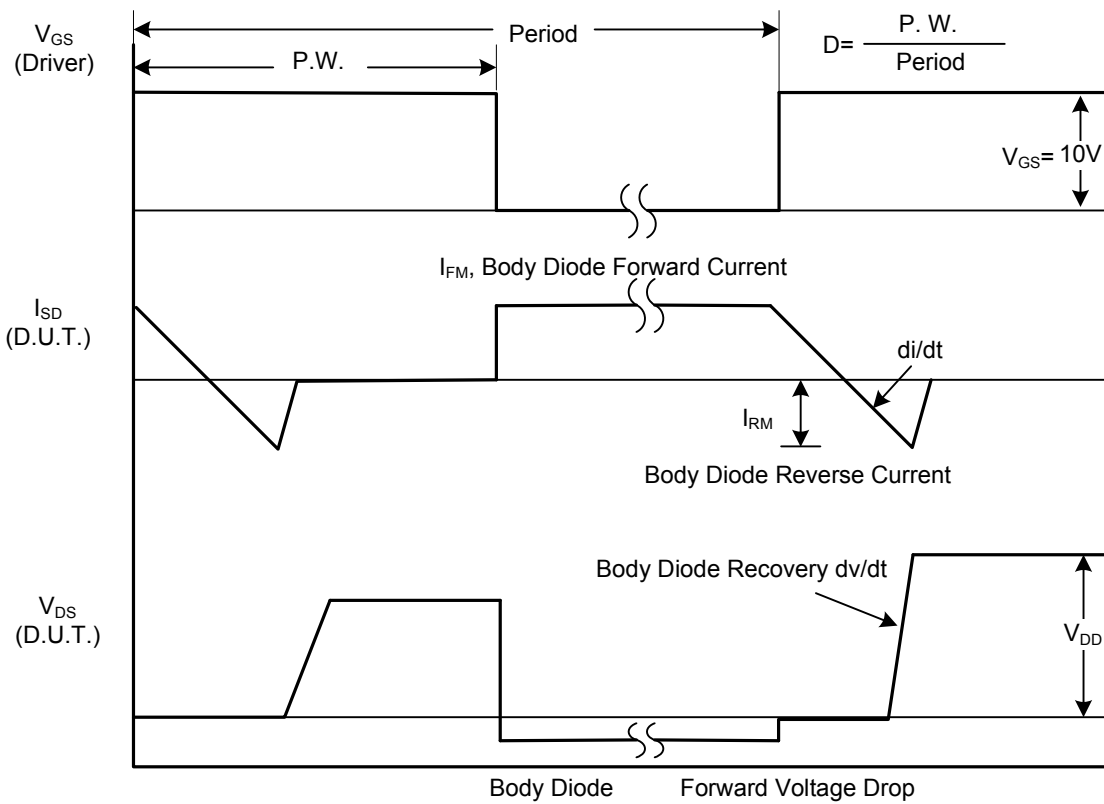
2. Essentially independent of operating temperature.

**650V N-Channel Power MOSFET**

**TEST CIRCUITS AND WAVEFORMS**



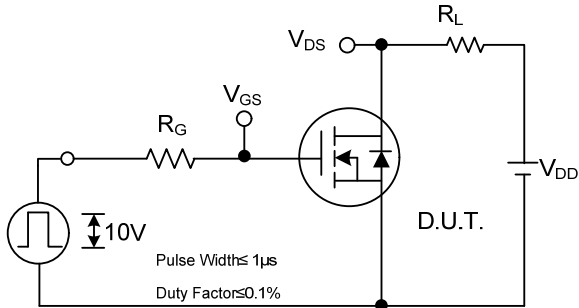
**Peak Diode Recovery  $dv/dt$  Test Circuit**



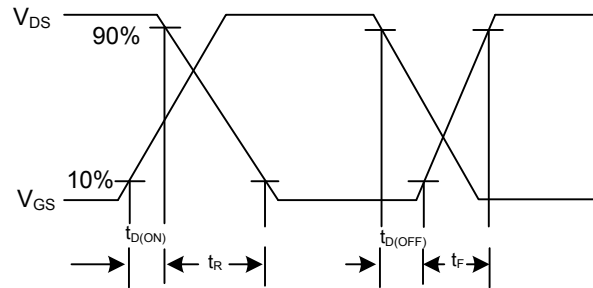
**Peak Diode Recovery  $dv/dt$  Waveforms**

**650V N-Channel Power MOSFET**

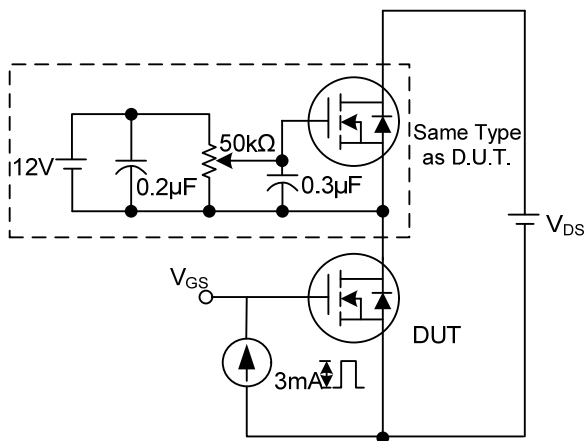
**TEST CIRCUITS AND WAVEFORMS(Cont.)**



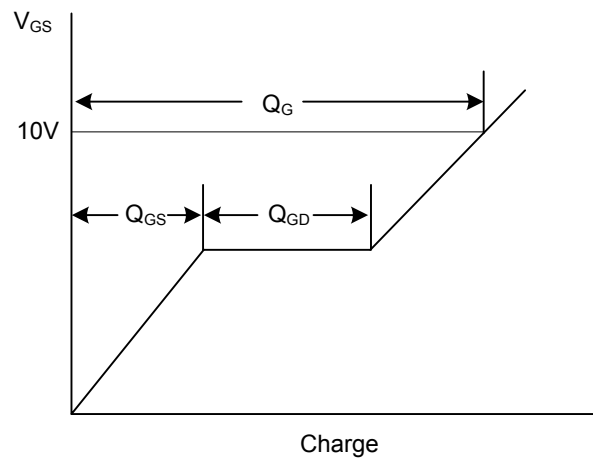
**Switching Test Circuit**



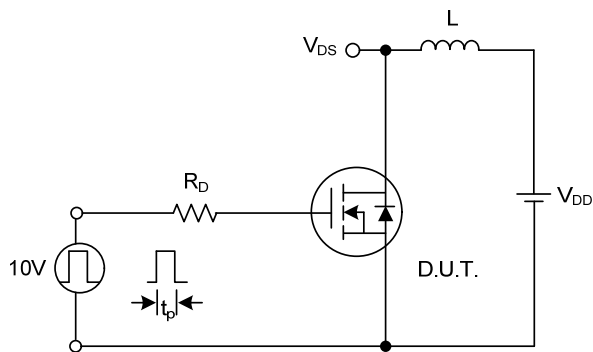
**Switching Waveforms**



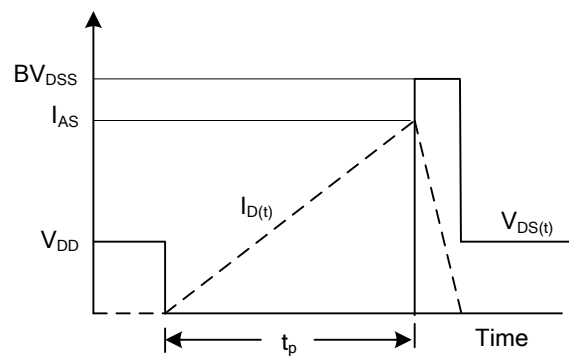
**Gate Charge Test Circuit**



**Gate Charge Waveform**



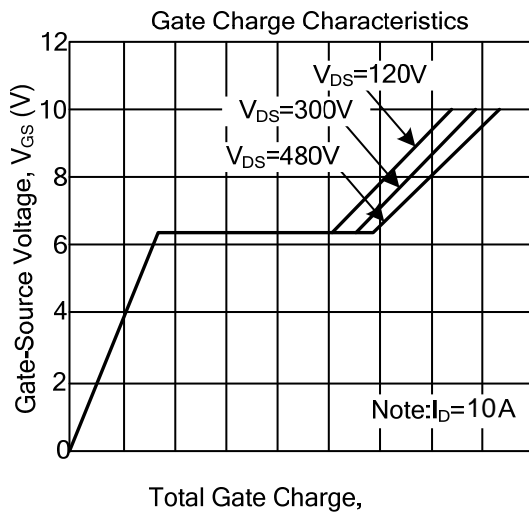
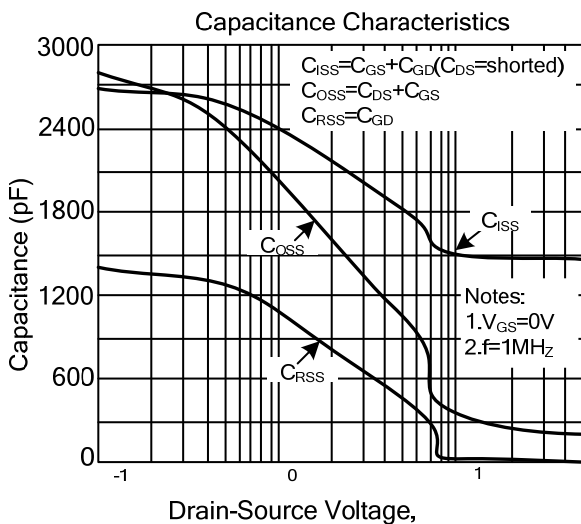
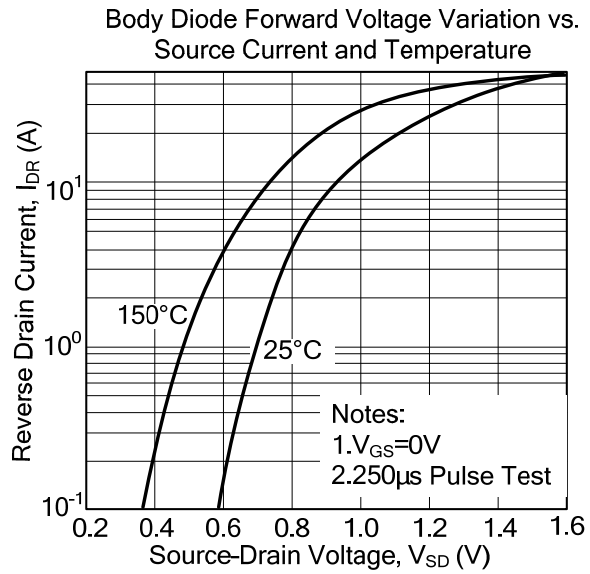
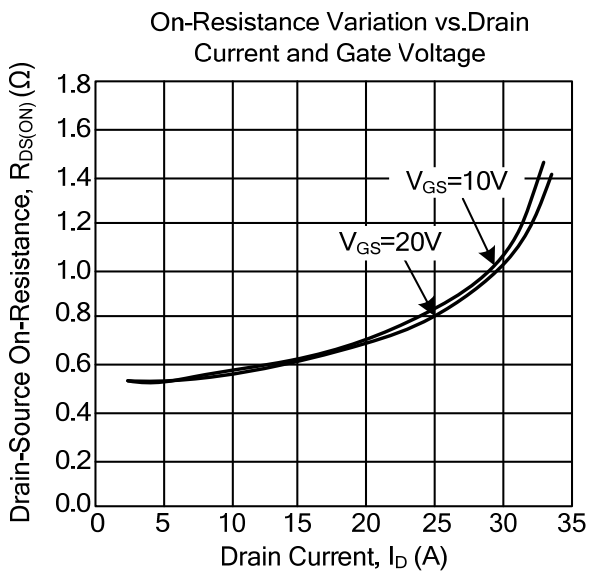
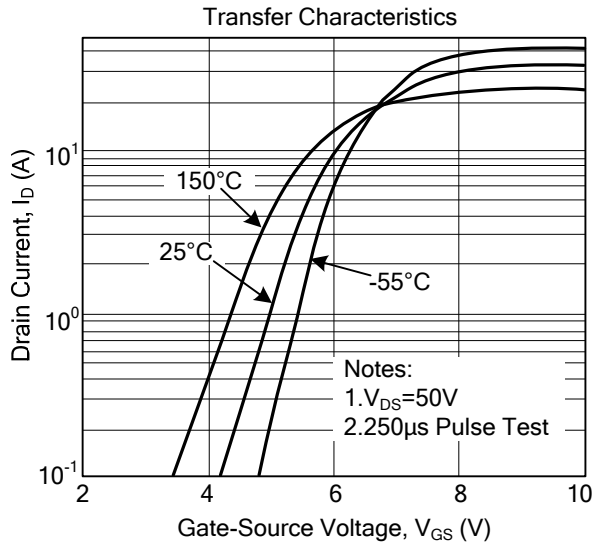
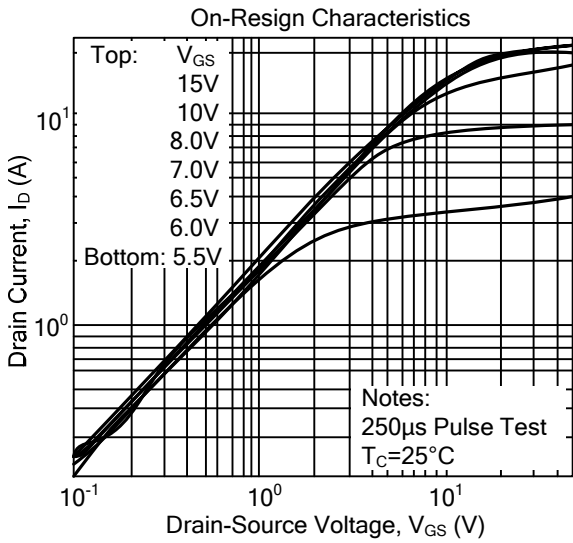
**Unclamped Inductive Switching Test Circuit**



**Unclamped Inductive Switching Waveforms**

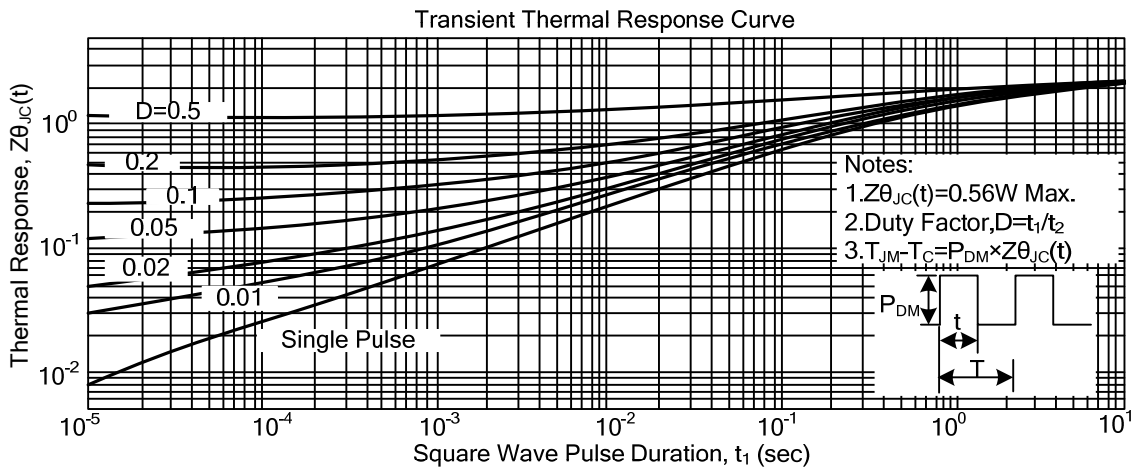
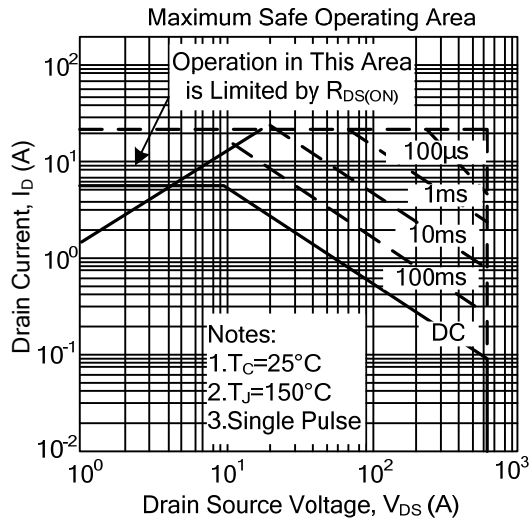
**650V N-Channel Power MOSFET**

**TYPICAL CHARACTERISTICS**

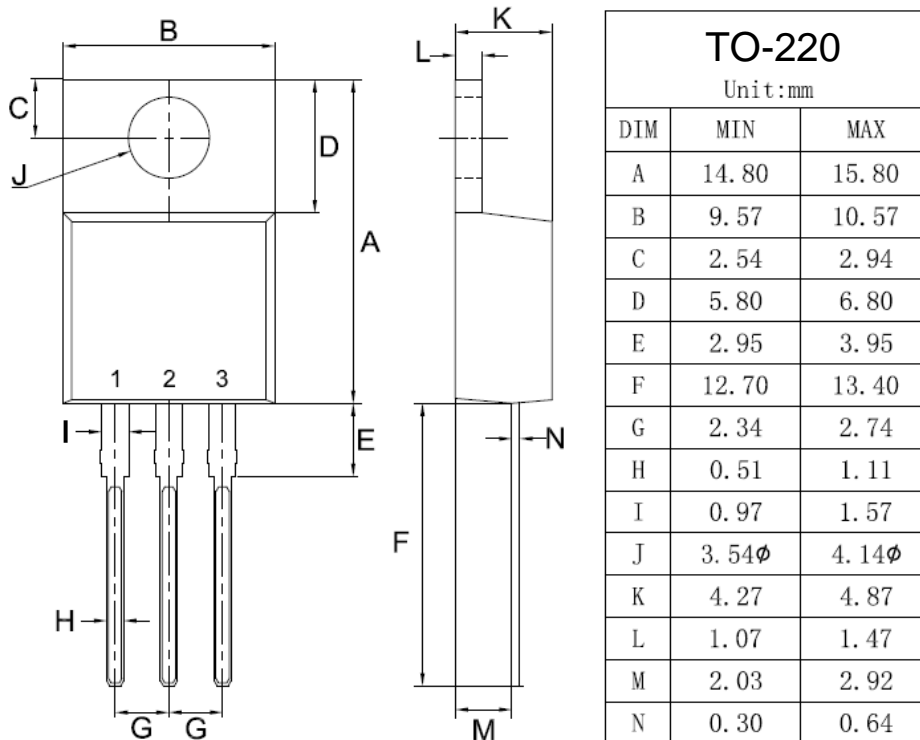


**650V N-Channel Power MOSFET**

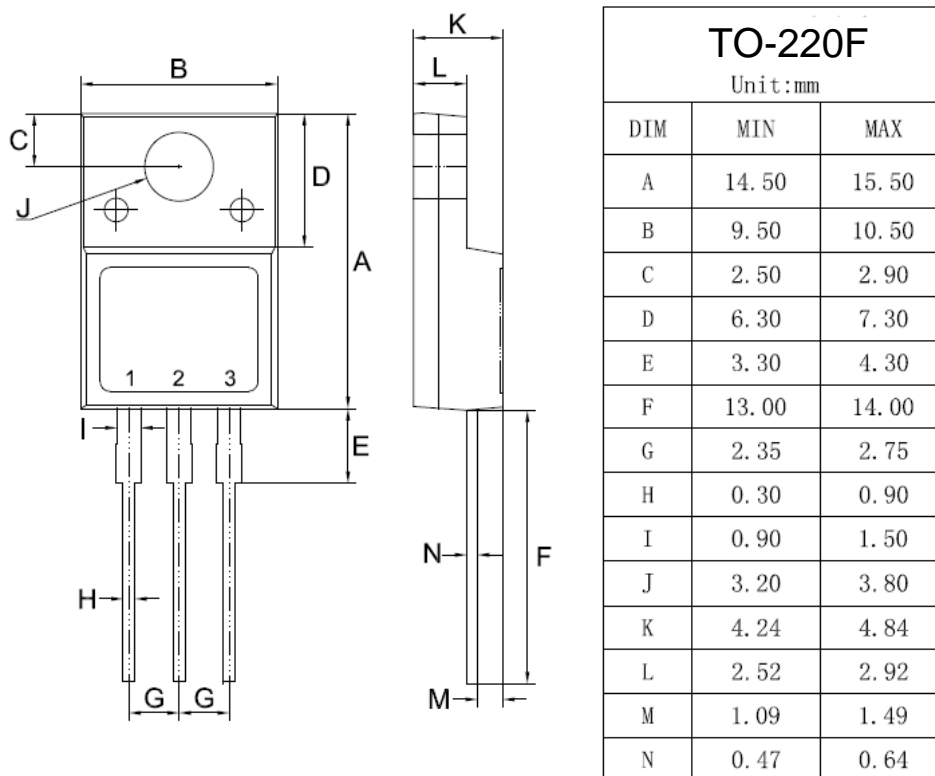
**TYPICAL CHARACTERISTICS**



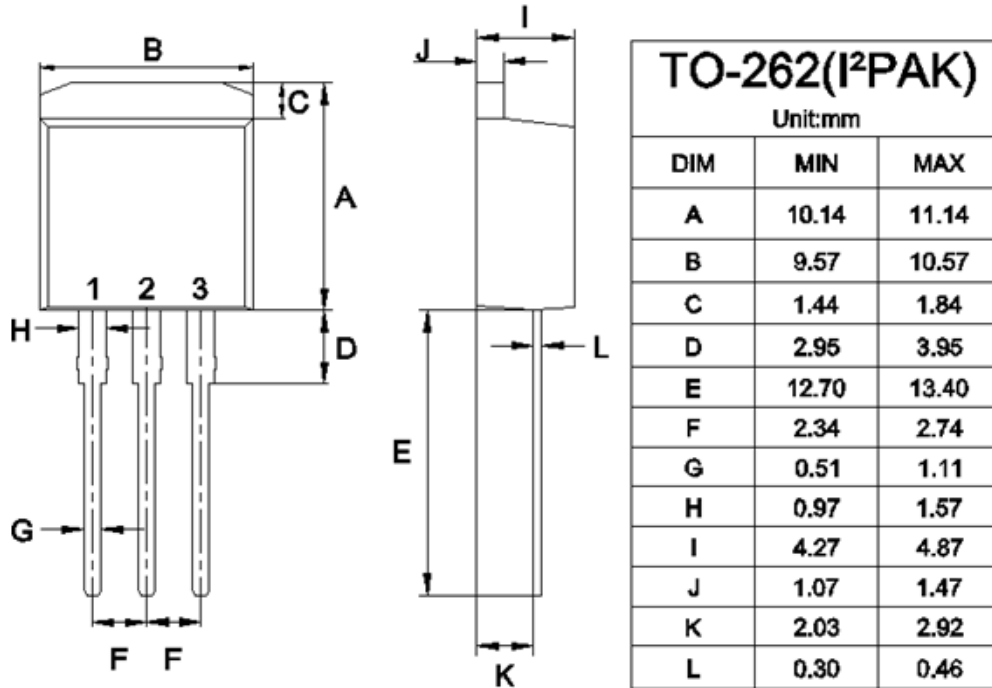
**TO-220 Mechanical Drawing**



**TO-220F Mechanical Drawing**



**TO-262 Mechanical Drawing**



**TO-263 Mechanical Drawing**

